

Adding and subtracting negative fractions pdf download

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$5 + (-3) = \underline{2}$

$6 + (-1) = \underline{5}$

$(-5) + (-6) = \underline{-11}$

$(-6) + (-2) = \underline{-8}$

$(-5) + 2 = \underline{-3}$

$(-9) + (-6) = \underline{-15}$

$8 + (-1) = \underline{7}$

$(-7) + (-4) = \underline{-11}$

$1 + (-7) = \underline{-6}$

$(-3) + 6 = \underline{3}$

$4 + (-4) = \underline{0}$

$(-5) + (-4) = \underline{-9}$

$(-2) + (-4) = \underline{-6}$

$(-5) + 8 = \underline{3}$

Total: 16

Goal: _____

Complete: _____

Correct: _____

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Adding & Subtracting Fractions

Name: _____

Solve each problem. Write the answer as a mixed number fraction (if possible).

Answers

1) $\frac{1}{2} - \frac{1}{2} =$

2) $\frac{4}{6} - \frac{2}{6} =$

3) $\frac{3}{6} - \frac{1}{6} =$

4) $\frac{9}{10} - \frac{1}{10} =$

5) $\frac{5}{10} - \frac{3}{10} =$

6) $\frac{2}{6} - \frac{1}{6} =$

7) $\frac{3}{4} - \frac{2}{4} =$

8) $\frac{8}{10} - \frac{2}{10} =$

9) $\frac{8}{12} - \frac{1}{12} =$

10) $\frac{6}{10} - \frac{3}{10} =$

11) $\frac{2}{5} + \frac{2}{5} =$

12) $\frac{1}{5} + \frac{1}{5} =$

13) $\frac{1}{3} + \frac{2}{3} =$

14) $\frac{2}{12} + \frac{3}{12} =$

15) $\frac{4}{8} + \frac{1}{8} =$

16) $\frac{6}{12} + \frac{5}{12} =$

17) $\frac{3}{4} + \frac{3}{4} =$

18) $\frac{3}{4} + \frac{2}{4} =$

19) $\frac{2}{10} + \frac{9}{10} =$

20) $\frac{9}{10} + \frac{4}{10} =$

1. _____

2. _____

3. _____

4. _____

5. _____

6. _____

7. _____

8. _____

9. _____

10. _____

11. _____

12. _____

13. _____

14. _____

15. _____

16. _____

17. _____

18. _____

19. _____

20. _____

Name : _____

Adding Simple Fractions

Can you add fractions with the same denominator?

$$\frac{1}{5} + \frac{3}{5} = \text{---}$$

$$\frac{4}{13} + \frac{7}{13} = \text{---}$$

$$\frac{1}{4} + \frac{2}{4} = \text{---}$$

$$\frac{2}{7} + \frac{4}{7} = \text{---}$$

$$\frac{5}{11} + \frac{2}{11} = \text{---}$$

$$\frac{8}{18} + \frac{10}{18} = \text{---}$$

$$\frac{9}{15} + \frac{2}{15} = \text{---}$$

$$\frac{2}{9} + \frac{3}{9} = \text{---}$$

$$\frac{6}{14} + \frac{3}{14} = \text{---}$$

$$\frac{3}{8} + \frac{3}{8} = \text{---}$$

$$\frac{12}{21} + \frac{6}{21} = \text{---}$$

$$\frac{8}{13} + \frac{2}{13} = \text{---}$$

$$\frac{3}{19} + \frac{7}{19} = \text{---}$$

$$\frac{14}{25} + \frac{5}{25} = \text{---}$$

Subtracting Negative Fractions (E)

Name: _____

Date: _____

Calculate each answer.

1. $-\frac{1}{2} - -\frac{1}{3} =$

2. $\frac{1}{3} - -\frac{3}{4} =$

3. $\frac{3}{5} - -\frac{4}{5} =$

4. $\frac{3}{4} - -\frac{1}{4} =$

5. $-\frac{1}{2} - -\frac{5}{6} =$

6. $-\frac{1}{4} - \frac{1}{4} =$

7. $-\frac{5}{6} - \frac{1}{6} =$

8. $\frac{1}{2} - -\frac{1}{3} =$

9. $-\frac{1}{4} - -\frac{3}{4} =$

10. $-\frac{5}{6} - \frac{1}{2} =$

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7. $-\frac{2}{5} + \frac{3}{4}$

8. $\frac{1}{6} - \frac{2}{9}$

9. $- \left(-\frac{2}{9} \right) + \frac{3}{7}$

10. $-\frac{1}{12} + \frac{3}{8}$

68012 Generation two (internally fully 32-bit) Motorola 68020 Motorola 68EC020 Motorola 68030 Motorola 68EC030 Generation three (pipelined) Motorola 68040 Motorola 68EC040 Motorola 68LC040 Generation four (superscalar) Motorola 68060 Motorola 68EC060 Motorola 68LC060 Others Freescale 683XX (CPU32 aka 68330, 68360 aka QUICC) Freescale ColdFire Freescale DragonBall Philips 68070 Improvement history 68010: Virtual memory support (restartable instructions) Loop mode for faster string and memory library primitives Multiply instruction uses 14 clock ticks less 68020: 32-bit address & arithmetic logic unit (ALU) Three stage pipeline Instruction cache of 256 bytes Unrestricted word and longword data access (see alignment) 8AA multiprocessing ability Larger multiply (32AA32 -> 64 bits) and divide (64A-A32 -> 32 bits quotient and 32 bits remainder) instructions, and bit field manipulations Addressing modes added scaled indexing and another level of indirection Low cost. EC = 24-bit address 68030: Split instruction and data cache of 256A Bytes each On-chip memory management unit (MMU) (68851) Low cost EC = No MMU Burst Memory Interface 68040: and data cachs of 4 kb each six-stage pipe Floating point unit in chip (FPU) FPU lacks the transcendental function of IEEE Emulation FPU works with 2e71m and subsequent chip reviews Low cost Lc = No FPU Low cost Ec = No FPU or mmu 68060: LPIN51 - PPIN20 -Instruction program of 8 kb every 10 phases Two multiplication units of two prediction prediction of the production branch of dual instructions instructions in the direction generation unit (AGU) and for what Both supply the result two cycles before the ALU LOW COS Lc = No FPU Low cost Ec = no FPU or mmu Map of characteristics AM € 1987 68030 132-PIN QFP (Max 33 MHz), 128-PIN PGA [5] 16â €" 50 32 mmu 68881 68EC030 132-PIN QFP, 128-PIN PGA 25 32 â €" 68881 1991 68040 179-PIN PGA33 , [6] 184 -pin Qfp [7] 20â €" 40 32 mmu FPU 68lc040 PGA the Gâ €" Nesis Sega used a 68000 clock at 7.67 MHz as its main CPU. 194 ~ karpinski pp. ISBN 978-0-471-85947-5. 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